

HISTELCON 2019

Organising Committee

in preparation, including

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Local Arrangements Committee

in preparation

Overview: HISTELCON is an IEEE Region 8 conference held approximately every two years, covering topics in the area of Technology History. The first HISTELCON was in September 2008, in Paris, France, with the theme “History of Telecommunications” and including topics in the history of electrical and information technologies. It was organised by the IEEE France Section and SEE (Societe des Electricite de l'Electronique et des Technologies de l'Information). Subsequent HISTELCONs were in Madrid, Spain (2010), Pavia, Italy (2012), Tel-Aviv, Israel (2015), and in August 2017 in Kobe, Japan.

Decision: The IEEE Region 8 Committee has decided that HISTELCON 2019 should be held in the IEEE UK and Ireland Section. Locally, it is proposed that it should probably be at Strathclyde University, Glasgow, Scotland, in August 2019 or early September 2019. Some other locations are also under discussion including Belfast, Northern Ireland.

The proposed primary theme is ‘historic computers’ with an aim to include papers on those inventions and developments which have not already been the subject of extensive historical publications, and to include the contribution special purpose processors have made to the development and use of advanced digital signal processing methods in many applications areas. Sessions to cover other aspects of technology history in the electrical, electronic and related fields will also be provided for.

In accordance with the traditions of previous HISTELCONs, the conference will include some invited keynote lectures as well as submitted, reviewed contributions.

Among the computer topics which might be included are, for examples, the history of the F100L and the Viper microprocessor, the Mascot real-time software design method, the Transputer, the ARM computer designs, and the impact of digital signal processing microprocessors including the TMS 320 for the implementation of very sophisticated signal processing algorithms.

The aim will be to keep the conference affordable, with reduced registration fees for IEEE members and students and members of co-sponsoring organisations.

Collaboration with the following other organisations is being discussed:

British Computer Society, The Computer Conservation Society, The Institution of Engineering and Technology (History of Technology Network and IET Archives), The National Museum of Computing, The British Science Museum (London), Museum of Science and Industry (Manchester), The LEO Computer Society, VMARS, DEHS, Cryptos.

