

4. September 11

Prof. Dragica Vasileska, IEEE Fellow, ASU

Lecture title:

Trends in low-power and high-power electronics, computational electronics and online education

Activity as a part of cooperation between Ss. Cyril and Methodius University in Skopje and UN International Organization for Migration (IOM) for engagement of Macedonian Academic Scientific Diaspora.

Topics Covered:

- Semiconductor trends.
- industrial automation systems powered by semiconductors and IoT.
- simulation methodologies and products useful for the broader engineering community.
- education programs at Arizona State University and NanoHUB as a tool for teaching and student learning
- simulation tools available free on NanoHUB.

We invite academic staff from the faculties at the UKIM Technical Campus, especially early-career researchers, to attend.



Prof. Dragica Vasileska



Vice Rector of the SS Cyril and Methodius University addressing the attendants

Dragica Vasileska (IEEE F'2019) received B.S.E.E. and M.S.E.E. Degree from the University Sts. Cyril and Methodius (Skopje, Republic of North Macedonia) in 1985 and 1991, respectively, and a Ph.D. Degree from Arizona State University in 1995. From 1995 until 1997, she held a faculty research associate position within the Center of Solid-State Electronics Research at Arizona State University. In the fall of 1997, she joined the faculty of Electrical Engineering at Arizona State University. In 2002 she was promoted to associate professor, and in 2007 to full professor. Her research interests include semiconductor device physics and semiconductor device modeling. Recently, her research interests also include modeling metastability and reliability of solar cells. Prof. Vasileska published more than 180 publications in prestigious scientific journals, over 200 conference proceedings refereed papers, 25 book chapters, has given numerous invited talks and is a co-author on three books: "Computational Electronics," D. Vasileska and S. M. Goodnick, Morgan & Claypool, 2006; "Computational Electronics: Semiclassical and Quantum Transport Modeling," D. Vasileska, S. M. Goodnick and G. Klimeck, CRC Press, 2010, and "Modeling Self-Heating Effects in Nanoscale Devices," K. Raleva, A. Shaik, D. Vasileska and S. M. Goodnick, Institute of Physics Publishing, Morgan & Claypool, 2017. She is also an editor of two books: "Cutting Edge Nanotechnology," In-Tech, 2010 and "Nano-Electronic Devices: Semiclassical and Quantum Transport Modeling" (co-editor S. M. Goodnick), Springer, July 2011. Prof. Vasileska is a recipient of the 1998 NSF CAREER Award. Her students have won numerous awards at prestigious international scientific conferences.